

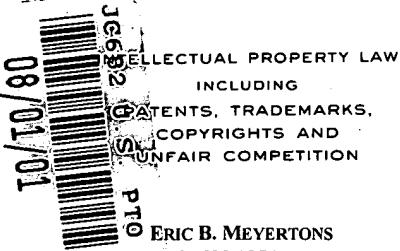
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FILE: 5119-08301

August 1, 2001

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Derrick Brown

ATTN: PATENT APPLICATION
Commissioner for Patents
Washington, D.C. 20231

Re: U.S. Patent Application Entitled "**METHODS FOR HIGH-PRECISION GAP AND ORIENTATION SENSING BETWEEN A TRANSPARENT TEMPLATE AND SUBSTRATE FOR IMPRINT LITHOGRAPHY**"
-- Choi, et al.

Sir:

Transmitted herewith for filing is a disclosure including a title page, a 53 page specification, 43 pages of claims (Claims 1-245), and a one page abstract. In addition, we have also included 52 drawings on 52 sheets. The disclosure and drawings constitute the application of Byung J. Choi, Matthew Colburn, S.V. Sreenivasan, C. Grant Willson, Todd Bailey and John Eckerdt for the above-entitled invention.

Please note that this application is filed without an inventor Declaration and filing fees. Applicant requests the Patent and Trademark Office to accept this application and accord a serial number and filing date as of the date this application is deposited with the U.S. Postal Service for Express Mail. Further, the Applicant requests that the NOTICE OF MISSING PARTS-FILING DATE GRANTED be sent to the undersigned Applicant representative.

Commissioner for Patents
August 1, 2001
Page 2

This application claims priority to U.S. Provisional Patent Application No. 60/222,233 filed on August 1, 2000 entitled "Methods for High-Precision Gap and Orientation Sensing between a Transparent Template and Substrate for Imprint Lithography."

Please address all correspondence in connection with this application to:

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Please date stamp and return the enclosed postcard to evidence receipt of this application.

Respectfully submitted,



Mark R. DeLuca
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Patent Agent for Applicant

EBM:etg
Enclosures

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METHODS FOR HIGH-PRECISION GAP AND ORIENTATION SENSING
BETWEEN A TRANSPARENT TEMPLATE AND SUBSTRATE FOR IMPRINT
LITHOGRAPHY

By:

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Matthew Colburn

S.V. Sreenivasan

C. Grant Willson

Todd Bailey

John Eckerdt